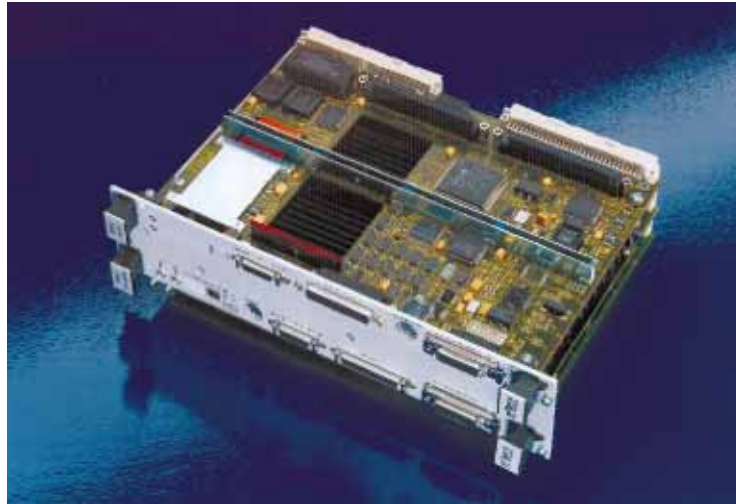


First Product to Offer 2eVME Support Provides Proven 110 MB/s Transfer Rate



SPARCstation® 20-compatible SPARC CPU-20VTe from FORCE COMPUTERS

FORCE COMPUTERS Inc. has announced the first VMEbus product to support the 2eVME protocol. The new SPARCstation® 20-compatible SPARC CPU-20VTe boasts 110 MB/s transfer rate on the VMEbus, giving the industry the highest VMEbus performance available in the durable SPARC V8 Mbus architecture.

The 2eVME protocol, specified in the VME64 Extensions Draft Standard, VITA 1.1-199X, increases available VMEbus bandwidth by reducing the number of handshake signals per transfer from four to two. This 50% reduction in protocol management is then allocated to the data transfer phases, effectively doubling maximum 2eVME bandwidth to 160 MB/s compared to 80 MB/s for MBLT transfers.

The SPARC CPU-20VTe achieves its industry-leading performance using FORCE's new SBus-to-VMEbus ASIC, the FGA-5100 (Download PDF file, 58KB). The FGA-5100 is an enhanced version of FORCE's leading-edge FGA-5000 ASIC, used on FORCE's SPARC VME CPUs since 1995. The FGA-5100 extends total system performance by combining 64-bit SBus and 2eVME capabilities. 64-bit SBus is supported by the Axil M2S Mbus-to-SBus interface used on the CPU-20VTe. The combination of on-board, 25 MHz, 64-bit SBus and 2eVME protocol support gives the CPU-20VTe the I/O bandwidth to pump data over the VMEbus at 110 MB/s.

Non-2eVME applications will benefit from the CPU-20VTe. The new FGA-5100 provides several internal performance enhancements — extra internal data buffers, faster DMA controllers and enhanced data prefetching among them — that effectively use the high-bandwidth of 64-bit SBus, boosting MBLT perfor-

mance by over 37%, from 40 MB/s with the FGA-5000 to 55 MB/s with the FGA-5100-based CPU-20VTe.

The SPARC CPU-20VTe is 100% compatible with Sun's SPARCstation 20 fami-

ly. The CPU-20VTe has 2 Mbus slots for one, two, or four, 150 or 200 MHz hyperSPARC processors, providing a wide range of price/performance options. Symmetric multiprocessing under Solaris, where all CPUs have equal access to the operating system, enables scalable processing power for both compute- and I/O-intensive applica-

tions.

FORCE COMPUTERS Inc., 2001 Logic Drive, San Jose, CA 95124, (972)-774-1888.

<http://www.force.computers.com>

VMEbus Training Classes Announced

UNDERSTANDING VME64 is VITA's one day training class. It provides a basic overview of VMEbus technology and is a good introduction for those new to the VMEbus or a good review for those with some previous experience.

VITA has tentatively scheduled classes for the following areas in the fall of 1997:

Greater San Francisco, August 21, 1997
Houston, Texas, September 18, 1997
Greater New York City, November 13, 1997

UNDERSTANDING VME64 can get you up on the learning curve as quickly as possible.

Morning Session

- VMEbus Evolution
- VME64
- Military VME
- Mechanical Standard
- Architecture
- Options

Afternoon Session

- Functional Modules
- Sub buses
- Bus Cycles
- Protocols
- Electrical Interface
- Problem Areas
- Futures

The fee for the one day training class is \$375.00 payable by VISA, MasterCard, or purchase order and includes a copy of the authoritative VMEbus Handbook, a copy of the VMEbus Product Directory and VITA Journal, a copy of all overheads in a convenient note taking format, a coupon for a 20% discount on the purchase of the VME64 Specification, and a personalized completion certificate.

Onsite training is also available!

Contact VITA at 602-951-8866 for registration information.