Contact Bob Downing, Fermi, rwd@fnal.gov, or Chris Parkman, CERN, chris_parkman@cern.ch, for more information regarding this effort. A task group ballot was completed on June 23, 1997 and the task group is currently reviewing the responses. A copy of the latest draft is available on the VITA web site.

VITA 25-199x, VISION- At the July 1996 meeting in Ottawa, Jim Pangburn of Fermi National Accelerator Laboratories presented a proposal for an object based I/O software interface for the VMEbus. To build consensus for this effort Jim asked and was granted study group status. At the September 1996 VSO meeting, task group status was granted when CERN, Lecroy, and Fermi indicated that they would sponsor this effort. A draft specification is available on the VITA web site. Contact Jim Pangburn, Fermi, pangburn@ fnal.fnal.gov, for more information on this effort.

VITA 26-199x, Myrinet- At the January 1997 meeting in Santa Clara, a new activity called Myrinet was proposed to the VSO. Myrinet is a high-speed (gigabit-per-second) packet communication and packet-routing technology that is used both as a system-area-network (SAN) and a local-area-network (LAN). Three VITA members, CSPI, AMP, and Myricom have agreed to support the standardization of Myrinet. Task group status has been granted and those interested in Myrinet should contact Jim Waggett at jwaggett@cspi.com.

VITA 27-199x, P2CI- At the July 1997 VSO meeting in Vancouver, BC, a new activity called P2CI was introduced by Robert Negre, CETIA. The purpose of this effort is the mapping of the PCI bus onto the P2 VMEbus user defined pins. Task group status was granted in August and the group is developing its initial draft. Contact Robert Negre, CETIA, rn@cetia.fr, for more information about this effort.

VSO STUDY GROUPS

VPCI- At the July 1997 meeting in Vancouver, BC, Rick O'Connor, Tundra, discussed the feasibility of creating a virtual PCI bus across the VMEbus backplane. This concept would allow a VME module with a PCI local bus to communicate directly with another VME module with a PCI local bus using standard PCI based bridge software. Contact Rick O'Connor, Tundra, ricko@tundra.com, if you are interested in this effort.

TEMPE- Jim Botte, NORTEL, introduced a concept called TEMPE, Telecommunications and Enterprise Multimedia Platform and Environment. TEMPE is an amalgamation of standard technology, extensions to that technology, and proprietary technology licensed from multiple companies integrated, and presented as a new open standard. Contact Jim Botte, NORTEL, jbotte@nortel.ca

Small PMC- Hermann Strass, Technology Consulting and VITA's European Technical Coordinator, introduced the concept of Small PMC at the July 1997 VSO meeting. Small PMC is being promoted by VITA member, MEN Mikro Electronik. It is based on the IEEE P1386.1 draft standard refered to as PMC which is a mezzanine module based on the PCI bus. Contact Hermann Strass at TechCon.HStrass@t-online.de for more information on Small PMC.

New Products Gallery

Space limitations prevent us from publishing all announcements received from our member companies. Visit the New Products Gallery at www.vita.com for a complete collection of announcements. - Ed.

The development of a highly versatile, VME graphics controller designed to perform high-speed graphic and image processing tasks in harsh environment airborne, naval and land-based military and commercial applications, has been announced by **DY 4 Systems Inc.** The new SVME/DMV-783 features the powerful video processing capabilities of the Texas Instruments 320C80 processor. The new DY 4 board delivers in a single VME slot the functionality that previously required three or more boards. The board can simultaneously update dual displays, perform high resolution RGB/NTSC frame grabbing and gather high-speed digital data from radar sets and remote sensors. DY 4 is at www.dy4.com

Electronic Solutions has announced the introduction of its Dot Ten family of system components packaging which support VME64 Extensions systems that utilize the IEEE 1101.10 and IEEE 1101.11 mechanical standard. The packaging components based on Electronic Solutions' Dot Ten is for a new generation of board level computers using VME64 Extensions. Available are Electronic Solutions' Dot Ten Injector/Ejector handles, Shielded Front Panels, Subrack assemblies, Air Management Boards, ESD hardware and Programming keys. In addition to the mechanical components Electronic Solutions offers a full product offering of VME64 Extensions backplanes. Electronic Solutions is at www.zerocorp.com/elsol

FORCE COMPUTERS has announced its new Internet-based service package called Software Maintenance and Reference Tools (SMART). SMART allows users unlimited, 24-hour access to a comprehensive database of support tools and engineering utilities for FORCE applications. SMART is the first service package to be released as part of SolutionsPLUS, the new full service Customer Support Program from FORCE COMPUTERS. SMART empowers engineers to quickly access a wide range of solutions to resolve development issues and meet their individual application needs. FORCE COMPUTERS is at www.forcecomputers.com

GreenSpring Computers has announced the availability of a new high-speed fiber optic communication system which transfers data from node to node at speeds up to 10.6 Mbytes/sec. The IP-FiberIO IndustryPack solution consists of a fiber optic shared memory interface on a Type III double-wide IndustryPack mezzanine module. IP-FiberIO offers complete implementation within the IndustryPack hardware and requires no drivers, protocol stacks, or specialized software. GreenSpring is at www.greenspring.com

Adding a new dimension of flex-

ibility is the new HART1NG Universal Serial Bus Connector — Series A and B, recently introduced by **HARTING, INC. of North America.** Users will be particularly interested in these features: Plug and Play; Hot Swapping Capabilities; Computer / telephony integration; and Port consolidation. The new units permit transfer rates up to 12 Mb/s and support up to 127 devices. The USB has subchannel for 1.5 Mb/s signaling and allows daisy chaining of peripheral devices. Up to 5 m per cable segment, Isochronous and Asynchronous data transfer and built in power distribution for low power devices are important specifications. Contact HARTING Customer Service at 847 741-1500 or FAX 847- 741-8257 for complete information.

To meet the demand for an economical, high-throughput serial communication solution, **Computer Products Inc./ Heurikon Division** has introduced the BajaPPC—a new VME-based CPU board that offers a flexible, high performance platform capable of performing a number of key system functions including system control, network control, network bridging, protocol processing and data filtering. Combining the BajaPPC with PMC cards allows users to create a network bridge subsystem. For example, a PM/Link module can be used in one PMC slot and an ATM interface in the other PMC slot, bridging T1/E1 or signaling networks with an ATM network. This continuity of base board across the different functions within a system provides easier software development and economies of scale, which translates to lower overall system cost and shorter time to market. Heurikon is at www.heurikon.com

JANZ Announces: VMOD-P5—The VMOD-P5 is a high performance VMEbus computer based on Intel's Pentium CPU. It extends the already existing VMOD CPU series (VMOD-32, VMOD-40/60) at the upper performance range. It is targeted to embedded applications and features the MODULbus mezzanine concept, so that it can be equipped with 3(6) modules from a multitude of existing solutions. Therefore the VMOD-P5 can ideally be used as an intelligent I/O controller for analog, digital or field-bus connections. JANZ is at www.janzag.de

JOERGER ENTERPRISES, INC., announces a 16 Channel, 32 Bit, 40MHz Up, Down, Presettable Scaler packaged in a single width "VME" module, the Model VSC16. Each channel is completely independent and its operating mode is preset in a rereadable register for verification. The module has an interrupt capability and each channel can be enabled or disabled from participating. The module has both a gate signal and arm signal to provide more complete system control. An arm output signal is provided that can be used to arm one or more units. The arm output signal is set when the module is programmably armed and is reset by an internal stop signal, a counter reaching its preset condition, or a module reset. To visually indicate module operation LED's are provided for DTACK, GATE INPUT, ARM INPUT and an activity light for each channel to display if it is counting. A 10MHz crystal oscillator output is also provided to perform such features as timing and testing. The module has onboard switches to set its address and is available for ECL, NIM and TTL operation with the standard inputs and outputs TTL. "FPICS" software is available. e-mail: joerger@bocom.com

Logical Design Group has announced the availability of the VME-191, a Pentium-based single board computer designed for extended temperature and ruggedization for high performance industrial, commercial and telemetry applications. The VME-191 provides all of the features of a fully-configured PC/AT motherboard, along with additional features such as a built-in Ethernet interface, PMC interface, SCSI-2 interface, SVGA Video, and a compliant of standard desktop I/O including: one Parallel, and two Serial I/O ports, keyboard and mouse interface. Logical Design Group is at www.ldg.com

MEN Mikro Elektronik is presenting eight new M-Modules for industrial computer platforms like VMEbus, CompactPCI etc. in its new four-page update brochure which completes the current MModule catalog. The most interesting new products belong to a new group of DSP-based M-Modules. These three M-Modules are distinguished by fast data transfer rates and flexible application possibilites and they are of course optically isolated. The M59 is an analog input module with 16 bits resolution. The 4 channels are simultaneous sampling at a sampling rate of 100 kHz. This M-Module is equipped with 2 ADSP2181 which offer 160 Kbytes of memory. The M59 uses this internal RAM as a program memory and data buffer. The DSPs are transfering the calculated data to the host via DMA. In the same way 2 ADSP2181 are used on the binary I/O module M63. The M-Module counts 16 inputs and another 16 channels for use of inputs or outputs. The output current is 500mA per channel. The temperature acquisition module M70 is equipped with one ADSP2181 and 80 Kbytes memory. 4, 8 or 16 channels providing 2, 3 or 4-wire technique are available for PT100 or PT1000 measurement. Alternatively, 15 thermo coupling channels (Ni-Cr-Ni) are available. The different possibilities of temperature acquisition and the accuracy of the M70 are software-programmable. MEN Mikro is at www.men.de

Motorola Computer Group has announced a VME single board computer based on the PowerPC 750 processor. The MVME2700 uses the new third-generation PowerPC processor and MCG's PowerPlus Architecture to deliver outstanding system performance with the low level of power dissipation essential for many embedded applications. With a PowerPC 750 clocked at 233 MHz and 1 MB of level 2 cache, MVME2700 delivers performance estimated at 10.2 SPECint95 and 8.2 SPECfp95. The PowerPC 750 has several new features which boost performance. A separate 64-bit Level 2 cache bus, which can transfer data at almost double the previous cache clock rate, improves the processor performance while off-loading cache traffic from the main CPU/memory bus. Dual integer ALUs, 32 KB on-chip instruction and data caches and dynamic branch prediction increase processing power still further. This performance is achieved within a maximum device power dissipation of just 5 watts, simplifying cooling and contributing to reliability. Motorola Computer Group is at www.mot.com/computer

National Instruments has announced new Test Executives for its LabVIEW and LabWindows/CVI test development instrumentation software. The new Test Executives offer a host of new features, including the ability to call tests developed in a variety of programming languages, such as LabVIEW, LabWindows/CVI, C/C++, and Visual Basic. Other new features include a modular architecture so users can easily customize tests; advanced sequencing and subsequencing capabilities so engineers can package commonly used test sets into reusable subsequences; sequence-level debugging with breakpoint and single-step capability; and ASCII sequence file generation/loading for test plan documentation or ISO 9000 requirements. National Instruments is at www.natinst.com

Pentek, Inc. has announced a single 6U VMEbus-based board that allows systems integrators to easily connect VMEbus system components utilizing a rich variety of extremely high-

speed, industry-standard interfaces. The Model 6310 Intelligent Multi-Link Adapter deems the name 'intelligent' since it includes a 60 MHz Texas Instrument TMS320C40 DSP for set up, initialization and control of data transfers and optional signal processing tasks. A zero-wait 1 MB Giobal SRAM acts primarily as a staging area for transfers to and from the VMEbus, 'C40 or the PMC module, although it can be used to buffer data over the local TAbus. The Model 6310 features the following six industry-standard VME interfaces and peak transfer rates:
Fibre Channel - up to 100 MB/sec
Two G-link interfaces - up to 100 MB/sec
PCI Mezzanine Card (PMC) - up to 132 MB/sec
Front-panel data port (FPDP) parallel interface - up to 160 MB/sec
Six 'C40 Comm Ports - up 20 MB/sec each
VME slave interface - up to 32 MB/sec
Pentek is at www.pentek.com.

Primagraphics, the Cambridge (UK) based graphics and image processing specialist, has introduced its third generation image processing and display system. The cornerstone of the

new system, which has been in development for more than two years, is the C.A.T. board. The aptly named 6U double Eurocard is built around the powerful TMS320C80 multimedia video processor. The C80 offers a performance of up to 2 billion operations per second through the use of a RISC processor core with 4 integral DSP processors and fast block data copy hardware. Internal buses allow data to be transferred within the board at speeds of up to 400 Mbytes/sec. Email: njp@primag.co.uk

Radstone Technology has introduced its new PPC2 range of single board computers, which have been successfully tested to the extended Environmental Shock requirements of a major U.S defense program. The PPC2 is the latest addition to Radstone's family of software compatible PowerPC VME processor boards designed to meet advanced embedded systems requirements and is available in four build levels in both air-cooled and conduction cooled versions. Utilizing Radstone's PowerPC Reference Platform, or PReP architecture, the PPC2 offers an open platform standard that incorporates the widest range of COTS operating systems available, including UNIX and hard real time. Flexible scalability and portability are two distinct advantages of Radstone's PReP architecture. Radstone is at www.radstone.com.

VMIC continues to enhance the technology of its broad line of Reflective Memory products with its latest offering. The VMIVME-5589 Quad-Redundant Reflective Memory board is a special high-performance Reflective Memory network board featuring a quad-redundant fiber-optic network data path. It has the ability to switch between four separate fiber-optic links when signal integrity is lost on the active network connection. This board can be used in conjunction with Automatic Node Bypass Boards (VMIVME-5593), to form a network "hub" of redundant Reflective Memory. VMIC is at www.vmic.com

APPOINTMENTS

HARTING INC. has elected Mr. Robert A. Hays, President and Chief Operating Officer of its U.S. subsidiaries. Mr. Hays will succeed Mr. H.P. Tillmann who will become Chairman of the Board. Mr. Tillmann stated that in addition to overseeing HARTING, INC. and HARTING, INC. of North America, Mr. Hays will have full responsibility for HARTING, INC. Manufacturing. Mr. Hays brings a wealth of experience to HARTING. He received a Masters Degree in Business from Washington University, and his Master of Science in Mechanical Engineering from the University of Missouri. Mr. Hays was involved in manufacturing and quality assurance during his tenure with the Monsanto/MEMC Electronic Materials Co., Inc. He has been serving as Vice President of Worldwide Marketing and Sales for the Electronics and Memory Product Division of EMPAK, INC. prior to joining HARTING.

PEP Modular Computers, Inc. of Pittsburgh, PA has announced the appointment of Tom P. Hall to the position of President and CEO. He will have full P&L responsibility for the company's North American operations. PEP plans to capitalize on Hall's strong sales and marketing experience, as well as his industry reputation as a pioneer in Mil-Spec VMEbus standard product. With his addition, PEP plans to ensure its leadership position within the changing U.S. marketplace. PEP Modular Computers is a leading international supplier of computer hardware, targeting the industrial, telecommunications and severe environment marketplace. It is also a sponsor member of VITA and plays an active role in several other trade and standards organizations such as PROFIBUS, PLCopen and PICMIG.

SKY Computers, Inc. has announced the appointment of Dr. Wanda Reiss as vice president of engineering. Dr. Reiss will direct SKY's on-going and new product engineering for SKY under the direction of Robert Hoenig, general manager and chief technical officer. SKY's engineering organization has an industry-wide reputation for utilizing the latest technologies and techniques to deliver superior performing and the highest quality products. Dr. Reiss holds a Ph.D. EE from the Technical Institute of Gdansk in Poland, as well as both a MSEE and a BSEE from the Technical Institute of Gdansk in Poland. Prior to joining SKY, Dr. Reiss was with Polaroid Corporation, most recently as a department and technical product Manager in the imaging products group. Dr. Reiss has also held Assistant Professor positions at Northeastern University in Boston and the Technical Institute of Gdansk in Poland.

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