

# VME64x Live Insertion

Lou Francz

Task Group Chair

Mike Thompson, Editor

# Ballot Summary

- 8 Returns out of 30 registered <30%
- Ballot Invalid
- Many valid comments - thanks
- Daisy chain bypass needs work
- More than just “clean-up”
- General “framework” not good enough

# Options for Daisy Chain Bypass

- Bypass “switch” may cause latch-up
- Race Conditions possible
- Attempt to design synchronous bypass logic? (simulation?)
- Mandate centralized arbiter?
- Restrict L. I. to Systems w/o INT or BRQ?
- Ideas?

# Other Comments/Issues

- Need rigorous state definitions
- L.I. Spec. too implementation-specific
- “Ready-to-pull” LED color
- Replace LI/I\* for “old” GND pin
- Remove HAVME and T&M bus ref’s.

# Chairmanship Issues

- Needs to be more active in resolving issues
- Regular Conf. Calls and active reflector
- Nominations? Volunteer?

# VME64xli Outlook

- Need for VME L.I. more critical than ever
- Needs to move more quickly
- A unified Software model would help
- Key an eye on known I.P.

# VITA 1.4-199x

- Draft 0.4 WORD97 file on [www.VITA.com/VSO...](http://www.VITA.com/VSO...)
- Reflector: [vme64xli@vita.com](mailto:vme64xli@vita.com)
- Chair: [l.francz@dialogic.com](mailto:l.francz@dialogic.com)