

# VME System Management Bus Proposal

Create a VITA document that is similar to the PICMG 2.9 System Management Specification. PICMG 2.9 uses IPMI as the base.

- Intelligent Platform Management Interface (IPMI)
  - Intelligent Chassis Management Bus (ICMB)
  - Intelligent Platform management Bus (IPMB)
    - I<sup>2</sup>C bussed on backplane. I<sup>2</sup>C inside chassis for PSU and alarms
    - IPMB Address Allocation
  - Platform Event Trap Specification
  - IPMI Platform Management FRU Information Storage Definition

IPMI information is at: <http://developer.intel.com/design/servers/ipmi/>

To Do:

- Assign bussed pins for IPMB\_SCL, IPMB\_SDA, IPMB\_PWR (+5 Stdbby)
- Assign unbused pins for SMB\_SCL, SMB\_SDA, SMB\_ALERT#
- Simulate I<sup>2</sup>C bus for 21 slots and Live Insertion