



VITA 34

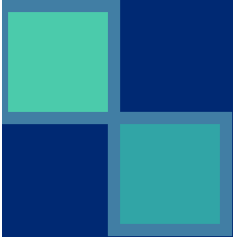

Scalable Modular Architecture for
High Speed Serial Interconnect for
Embedded System Architectures



Robert Downing – Chair
Scottsdale - 23 May 2002




VITA 34 Goals

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- Enhancement of IEEE 1101.10
 - Refinement of mechanical architecture
 - Increased board space
 - Relaxed mechanical tolerances for ease of manufacture
 - Improved injector/extractor mechanism
 - Address higher cooling capabilities for increased board and power densities
 - Higher performance connectors
 - Improve EMI/RFI
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VITA 34


- Simplify
 - Specify only that which is needed
 - Focus on the goals
 - Use ANSI/VITA 1 specification as a model
 - Differentiate
 - Create a FAQ that explains our goals
 - Advertise
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VITA 34




- Mechanics

- Get a solid prototype modeled
 - Define what is included in a reference model
 - Standard (interface dimensions) vs. reference model (manufacturing example)
 - Create a functional prototype
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VITA 34

- Infrastructure Electronics
 - Maintenance bus – “simplified IPMI”
 - I²C with simplified protocol that works with existing power supplies, fans, etc.
 - Define an ISO layer model
 - I²C at the bottom
 - IPMI higher
 - SNMP top
 - Don't try to use the bottom layer for downloads and reconfigurations
 - Keep it simple at the lower levels
 - i.e, PS monitors, Fans, board on/off control
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VITA 34

- Infrastructure Electronics
 - Fabric
 - Pick a base fabric
 - Software major consideration
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VITA 34

- Re-Organization
 - Call for active participation
 - Target new time frame to coincide with market upswing
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