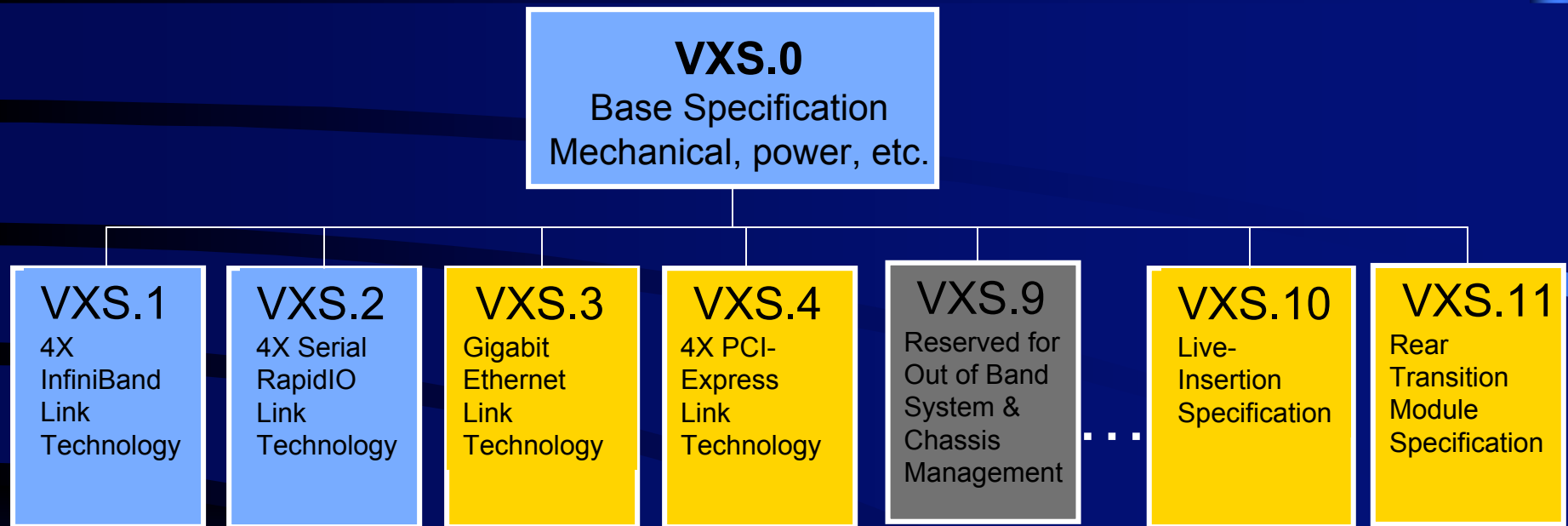


VITA 41 (VXS) Update

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VITA 41 (VXS) Document Structure & Status



 ANSI Standards

 Active Draft Specifications

 Reserved for future use

VITA 41 Status Update

- VITA 41.0 – Base Specification
 - ANSI ballot passed on 11/30/05
- VITA 41.1 – InfiniBand Protocol Specification
 - ANSI ballot passed on 11/30/05
- VITA 41.2 – RapidIO Protocol Specification
 - ANSI ballot passed on 11/30/05

VITA 41 Status Update

- VITA 41.3 – GbE Protocol Specification
 - Plans to go to ANSI in January 2006 as a trial use standard
- VITA 41.4 – PCI-Express Protocol Specification
 - Plans to go to ANSI in January 2006 as a trial use standard
- VITA 41.10 – Live Insertion Specification
 - Plans to go to ANSI in January 2006 as a trial use standard
- VITA 41.11 – Rear Transition Module Specification
 - Plans to go to ANSI in January 2006 as a trial use standard

VITA 41 Objectives

1.1 Objectives

The goals of this standard are:

- Add a switched serial interconnect to VMEbus coincident with the VMEbus parallel bus;
- Employ standard open technology for the switched serial links;
- Accommodate multiple standard open technologies for the links, but not necessarily at the same time;
- Maintain backward compatibility with the VMEbus ecosystem; and
- Bring more power onto each VMEbus card.

VITA 41 Market Acceptance

- The original goal was to invigorate the VME market through innovation & technology
- The VITA 41 working group has helped accomplish this goal
- VME's prosperity & longevity are important to all the working group members
- Market for VITA 41 products is flourishing
- Numerous companies have announced VITA 41 products