



**C – FPGA Mezzanine Module (VITA
D – Nov 2007 Update**

0 Mezzanine Module for FPGA systems

TA 57.1 (FMC)

Electromechanical Specification

TA 57.2

Metadata Description for physical connection of I/O

TA 57.3

FPGA Driver IP for VITA 57.1 module

G ballot completed

Ballot Summary

Approve	19
Disapprove	0
Abstain	3
No Return	26
Total	48

Ballot Status

Approved Ballots	100.00%
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Number of conference call held relating to
7.3

Work on these areas is ramping up, with i
proposals to be ready before next VSO