



# NEWS RELEASE

**FOR IMMEDIATE RELEASE:**

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## **Updates to FPGA Mezzanine Card (FMC) Standard Ratified**

**Revision adds support for high resolution imaging**

SCOTTSDALE, AZ, April 13, 2010 — VITA, the trade association dedicated to fostering American National Standards Institute (ANSI) accredited, open system architectures in critical embedded system applications announced the ratification by ANSI of recent updates to the FPGA Mezzanine Card standard under ANSI/VITA 57.1. This standard describes a small form factor I/O mezzanine card that can be connected to all leading carrier board form factors. It assumes that FMCs connect to an FPGA device or other device with reconfigurable I/O capability. The revision adds support for a wider range of applications that harness high speed serial interfaces including high resolution imaging and JEDEC JESD204A based data convertors.

The updated specification is available from VITA at [www.vita.com](http://www.vita.com).

Companies that develop FMC products are encouraged to contact VITA to join the FMC Marketing Alliance. For more information, visit the FMC Marketing Alliance website at [www.vita.com/fmc](http://www.vita.com/fmc).

### ***About VITA***

Founded in 1984, VITA is an incorporated, non-profit organization of suppliers and users who share a common market interest in critical embedded systems. VITA champions open system

architectures. Its activities are international in scope, technical, promotional and user-centric. VITA aims to increase total market size for its members, expand market exposure for suppliers, and deliver timely technical information. VITA has ANSI and IEC accreditation to develop standards (VME, VXS, VPX, OpenVPX, XMC, FMC, etc) for embedded systems used in a myriad of critical applications and harsh environments. For further information, visit [www.vita.com](http://www.vita.com).

*Source: VITA*